

R&D Engineer - IP and SoC Design

Location: Montpellier, France

Education: Engineer (Bac +5, Universities or engineering schools)

Experience: 0 to 2 years in integrated circuit design

Date of the offer: April 2019

Context

Algodone offers a platform of software-based services and hardware blocks for digital rights management in the field of integrated circuits.

Mission

You participate in the R&D team in the development of our hardware products, in particular the creation of cryptographic and signature generation components for FPGA and ASIC circuits.

Your curiosity, your involvement and your rigor are indispensable in this type of work to analyze and design new innovative technical solutions with high added value.

Technical skills

You have a first experience in the design of integrated circuits (digital and / or analog) and you understand:

- FPGA and ASIC methodologies and design flows
- The rules of "Design For Reuse"

You have good knowledge in:

- HDL Languages and Verification: VHDL, Verilog, SystemVerilog
- Front End Tools: modeling, functional verification, synthesis, DFT, formal verification
- Back End Tools: Routing Placement, DFM

Communication

You master technical English and have good oral and written communication skills.