

## **R&D Engineer – IP and SoC Design – HW Security**

Location: Montpellier, France

Education: Master degree level

Experience: 2 to 5 years in integrated circuit design

Date of the offer: April 2018

### **Context**

Algodone offers a service platform made of software and hardware blocks for digital rights management in the field of integrated circuits.

### **Mission**

As a member of the R&D team, you will work on tasks related to the specification, design and development of products.

You will participate in collaborative programs to select and integrate technological bricks of partners especially in the field of security: cryptography, identification, countermeasures.

You will support the sales team and facilitate the adoption of our products through technical support.

Your curiosity, your involvement and your rigor are essential in this type of work to analyze and design new innovative technical solutions with high added value.

### **Technical Skills**

You have demonstrated excellence in the design of digital integrated circuits and you master:

- SoC architectures
- ASIC and FPGA design flows and methodologies
- "Design For Reuse" best practices

You have strong knowledge in:

- Verification and HDL languages: VHDL, Verilog, SystemVerilog
- Front End tools: modeling, functional verification, synthesis, DFT, formal verification
- Back End tools: place and route, DFM

You are experienced in:

- Hardware and software security
- Embedded systems programming

### **Communication**

You master technical English and have good oral and written communication skills.